

WHAT IS CLAIMED IS:

1. A signal transmission circuit for transmitting signals from an input terminal section to which the signals are inputted, to a processing circuit section in which the signals are processed, the signal transmission circuit comprising:

a printed wiring board including input wiring connected to the input terminal section and an insulation board on which the input wiring is formed; and

a first capacitor, one terminal of which is connected to the input wiring,

a connection point between the first capacitor and the input wiring being closer to a connection point between the input terminal section and the input wiring than a connection point between the processing circuit section and the input wiring,

a portion of the input wiring extending from the connection point with the first capacitor to the connection point with the input terminal section being formed by only a conductive pattern.

2. A signal transmission circuit for transmitting signals from an input terminal section to which the signals are inputted, to a processing circuit section in which the signals are processed, the signal transmission circuit comprising:

a printed wiring board including input wiring connected to the input terminal section and an insulation board on which the input wiring is formed;

a transistor having a base terminal which is connected to the input wiring, an emitter terminal which is grounded, and a collector terminal which is connected to the processing circuit section;

a reference voltage source for supplying a predetermined reference voltage to the collector terminal of the transistor;

a resistor which is connected in parallel between the input wiring and the base terminal of the transistor, and the emitter terminal of the transistor; and

a first capacitor, one terminal of which is connected to the input wiring,

a portion of the input wiring extending from a connection point with the first capacitor to a connection point with the resistor including three or more conductive patterns which are respectively placed on both faces of the board and two or more through holes or via holes which connect the conductive patterns placed on both the faces of the board.

3. A signal transmission circuit for transmitting signals from an input terminal section to which the signals are inputted, to a processing circuit section in which the signals are

processed, the signal transmission circuit comprising:

- a printed wiring board including input wiring connected to the input terminal section, wiring for grounding, and an insulation board on which the input wiring and the grounding wiring are formed;

- a transistor having a base terminal which is connected to the input wiring, an emitter terminal which is connected to the grounding wiring, and a collector terminal which is connected to the processing circuit section;

- a reference voltage source for supplying a predetermined reference voltage to the collector terminal of the transistor;

- a resistor which is connected in parallel between the input wiring and the base terminal of the transistor, and the emitter terminal of the transistor; and

- a first capacitor, one terminal of which is connected to the input wiring,

- a portion of the input wiring extending from a connection point with the first capacitor to a connection point with the resistor being placed in a vicinity of the grounding wiring,

- a parasitic capacitance being generated between the portion and the grounding wiring.

4. A signal transmission circuit for transmitting signals from an input terminal section to which the signals are inputted,

to a processing circuit section in which the signals are processed, the signal transmission circuit comprising:

a printed wiring board including input wiring connected to the input terminal section and an insulation board on which the input wiring is formed;

a transistor having a base terminal which is connected to the input wiring, an emitter terminal which is grounded, and a collector terminal which is connected to the processing circuit section;

a reference voltage source for supplying a predetermined reference voltage to the collector terminal of the transistor;

a resistor which is connected in parallel between the input wiring and the base terminal of the transistor, and the emitter terminal of the transistor;

a first capacitor, one terminal of which is connected to the input wiring; and

a second capacitor, one terminal of which is connected to a portion of the input wiring extending from a connection point with the first capacitor to a connection point with the resistor,

a connection point between the second capacitor and the input wiring being closer to a connection point between the transistor and the input wiring than the connection point between the first capacitor and the input wiring,

a portion of the input wiring extending from the connection point with the first capacitor to the connection point with the second capacitor including two or more conductive patterns which are respectively placed on both faces of a board and one or more through holes or via holes which connect the conductive patterns placed on both the faces of the board.

5. An electronic equipment comprising:

an input terminal section to which a signal is inputted;

a processing circuit section in which a signal is processed; and

the signal transmission circuit of claim 1 that connects the input terminal section and the processing circuit section.

6. An electronic equipment comprising:

an input terminal section to which a signal is inputted;

a processing circuit section in which a signal is processed; and

the signal transmission circuit of claim 2 that connects the input terminal section and the processing circuit section.

7. An electronic equipment comprising:

an input terminal section to which a signal is inputted;

a processing circuit section in which a signal is

processed; and

the signal transmission circuit of claim 3 that connects the input terminal section and the processing circuit section.

8. An electronic equipment comprising:

an input terminal section to which a signal is inputted;

a processing circuit section in which a signal is

processed; and

the signal transmission circuit of claim 4 that connects the input terminal section and the processing circuit section.